

A circular stamp from the Office of Intellectual Property (OIPE). The text "OIPE" is at the top, "IAPB" is at the top right, "JAN 09 2008" is in the center, and "PATENT &amp; TRADEMARK OFFICE" is at the bottom.

[illegible]

A detailed cross-sectional view of a semiconductor device. The structure is built on a substrate 10, which contains a dashed line 11 and a solid line 12. Above the substrate is a layer 13, followed by a patterned layer 14. A central region contains a structure with a central block 15, flanked by regions 16a and 16b, and topped with a layer 17. Above this is a layer 18a, which is part of a larger region 18b. A layer 22 is on the left. A central region 20 is defined by a dashed line and labeled "light emitting region" with three upward-pointing arrows. This region is covered by a layer 24, which is topped by a patterned layer 23. The entire structure is capped by a layer 19, with a top surface 21 on the right. Various other layers and regions are indicated by numbers 10 through 24.